

Application No. Not Yet Assigned  
Amendment dated February 10, 2006  
First Preliminary Amendment

Docket No.: SON-3074

**REPLACEMENT SHEET**

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FIG. 3

FIG. 3 A

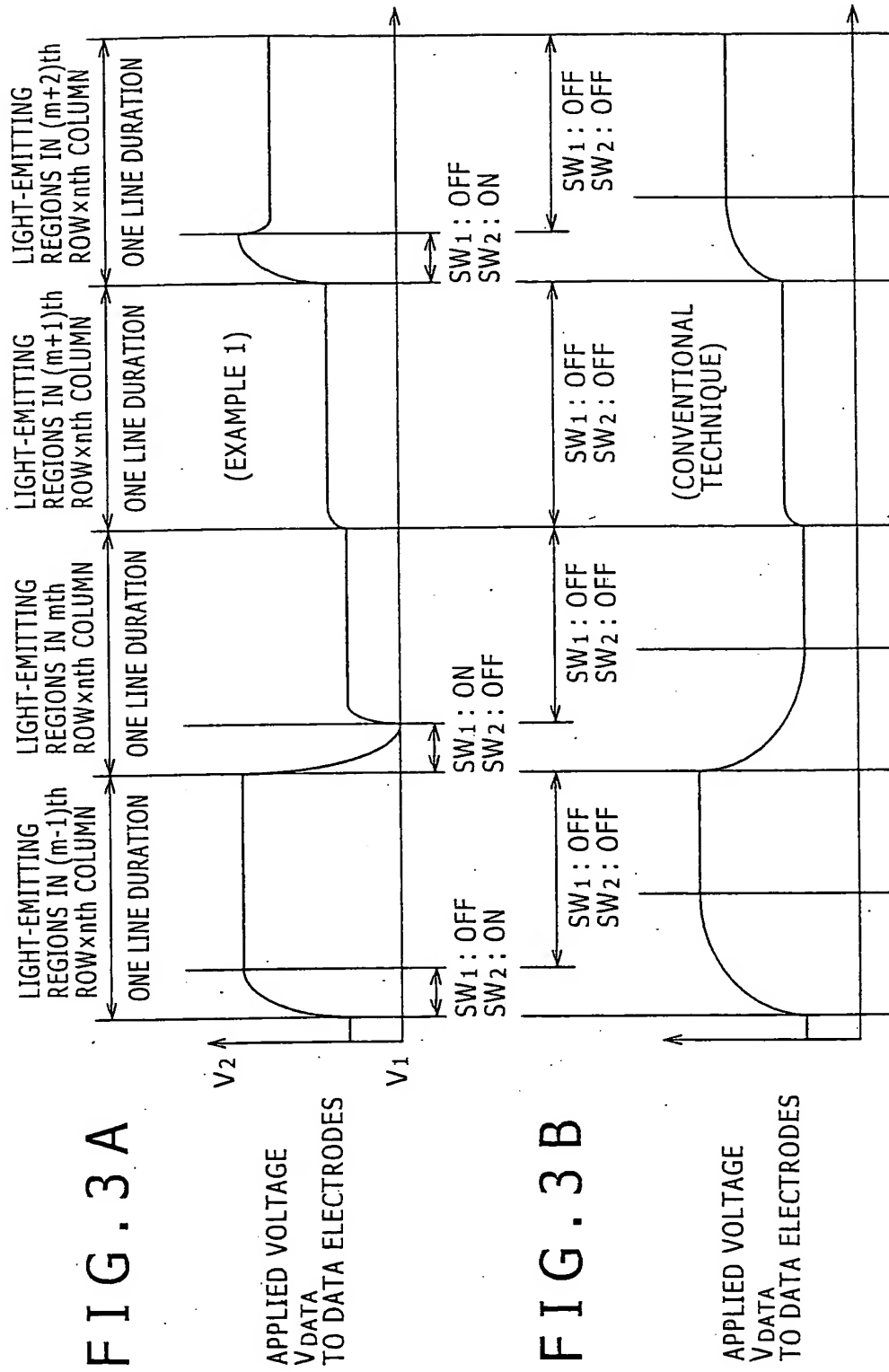


FIG. 3 B

APPLIED VOLTAGE  $V_{DATA}$  TO DATA ELECTRODES

FIG. 3 B illustrates the timing of the applied voltage  $V_{DATA}$  to the data electrodes for four light-emitting regions in the  $m$ th row and  $n$ th column, using a conventional technique. The regions are defined by their column indices:  $(m-1)$ th,  $m$ th,  $(m+1)$ th, and  $(m+2)$ th. Each region has a duration of "ONE LINE DURATION".

The timing diagram shows the voltage  $V_{DATA}$  (labeled  $V_1$  and  $V_2$ ) and the states of two switches,  $SW_1$  and  $SW_2$ , for each region:

- Region 1 (Light-emitting regions in  $(m-1)$ th ROW x  $n$ th COLUMN):  $SW_1$ : OFF,  $SW_2$ : OFF.
- Region 2 (Light-emitting regions in  $m$ th ROW x  $n$ th COLUMN):  $SW_1$ : OFF,  $SW_2$ : OFF.
- Region 3 (Light-emitting regions in  $(m+1)$ th ROW x  $n$ th COLUMN):  $SW_1$ : OFF,  $SW_2$ : OFF.
- Region 4 (Light-emitting regions in  $(m+2)$ th ROW x  $n$ th COLUMN):  $SW_1$ : OFF,  $SW_2$ : OFF.

(CONVENTIONAL TECHNIQUE)